



Pin No.	Symbol	Pin No.	Symbol	Pin No.	Symbol
1	GND	1	GND	1	GND
2	Host_RX	A+	2	Host_RX	2
3	Host_TX	B-	3	Host_TX	3
4	VCC	4	VCC	4	VCC
				5	RIGHT
				6	ENTER

备注	此模块与XFO70-WSVGA-ST-01-V04 共用PCB板
----	-------------------------------------

1	2	3	4	5	6	7	8
---	---	---	---	---	---	---	---

		SCALE		FREE	TOLERANCE	±0.2	深圳市尚视界科技有限公司 Shenzhen Sampixels Technology Co.,LTD PROJECT NDXXF070			
		UNIT	mm	DRG DATE	2025/07/19					
				DRAWN BY	TGG					
				DSN BY	TGG					
				CHECKED BY	LYQ					
				APPROVED BY	LYQ	DWG.XF070-VVGA1-ST-01-V01				
							ICDN	A4	EDITION	
							P 1	OF 1	A	